

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Application of: |) | |
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| Yoshiki ISHIZUKA et al. |) | |
| |) | Group Art Unit: Not Yet Assigned |
| Serial No.: Not Yet Assigned |) | |
| |) | Examiner: Not Yet Assigned |
| Filed: December 3, 2003 |) | |
| |) | |
| For: COLD CATHODE TYPE ELECTRON |) | |
| EMITTING DEVICE, METHOD OF |) | |
| MANUFACTURING THE SAME AND |) | |
| METHOD OF DRIVING THE SAME |) | |

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicants bring to the Examiner's attention the documents listed on attached Form PTO-1449. With the exception of the U.S. patent, copies of the listed documents are attached. Applicants respectfully request that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form. This Information Disclosure Statement is being filed with the above-referenced application.

The following is a concise statement of relevance of the non-English language document:

1. Japanese Patent Publication No. 11-297192 - discloses the properties of electron emitters and the methods to make them. The relevance of this document is also discussed at page 4 of the specification of the present application

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An English-language abstract of this document is also enclosed.

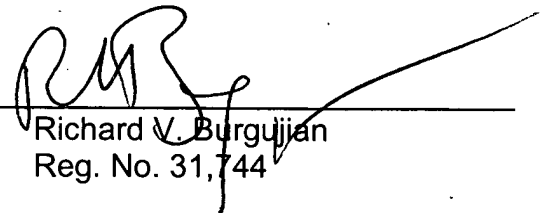
This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicants determine that the cited documents do not constitute "prior art" under United States law, applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such documents. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: December 3, 2003

By: 
Richard V. Burgujian
Reg. No. 31,744

Enclosures
RVB/FPD/sci

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INFORMATION DISCLOSURE CITATION

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| Atty. Docket No. | 04329.3187 | Serial No. | Not Yet Assigned |
| Applicants | Yoshiki ISHIZUKA et al. | | |
| Filing Date | December 3, 2003 | Group: | Not Yet Assigned |

| U.S. PATENT DOCUMENTS | | | | | | | |
|-----------------------|--|-----------------|------------|-----------------|-------|-----------|----------------------------|
| Examiner Initial* | | Document Number | Issue Date | Name | Class | Sub Class | Filing Date If Appropriate |
| | | 2002-0057046 A1 | 05/16/02 | YAMAMOTO et al. | | | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | |
|--------------------------|--|-----------------|------------------|---------|-------|-----------|-----------------------|
| | | Document Number | Publication Date | Country | Class | Sub Class | Translation Yes or No |
| | | 11-297192 | 10/29/99 | JAPAN | | | ABSTRACT |
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| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) | |
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| | BISCHOFF, M. et al., "Energy Distribution of Emitted Electrons From Electroformed MIM Structures: The Carbon Island Model", Int. J. Electronics, Vol. 73, No. 5, pp. 1009-1010, (1992). |
| | BISCHOFF, M., "Carbon-Nanoslit Model For the Electroforming Process in M-I-M Structures", Int. J. Electronics, Vol. 70, No. 3, pp. 491-498, (1991). |
| | PAGNIA, H. et al., "Scanning Tunnelling Microscopic Investigations of Electroformed Planar Metal-Insulator-Metal Diodes", Int. J. Electronics, Vol. 69, No. 1, pp. 25-32, (1990). |
| | PAGNIA, H., "Metal-Insulator-Metal Devices With Carbonaceous Current Paths", Int. J. Electronics, Vol. 69, No. 1, pp. 33-42, (1990). |
| | PAGNIA, H. et al., "Bitable Switching in Electroformed Metal-Insulator-Metal Devices", Review Article: Phys. Stat. Sol. (a)108, 11, pp. 11-65, (May 3, 1988). |
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| Examiner | Date Considered |
| *Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | |
| Form PTO 1449 | Patent and Trademark Office - U.S. Department of Commerce |